

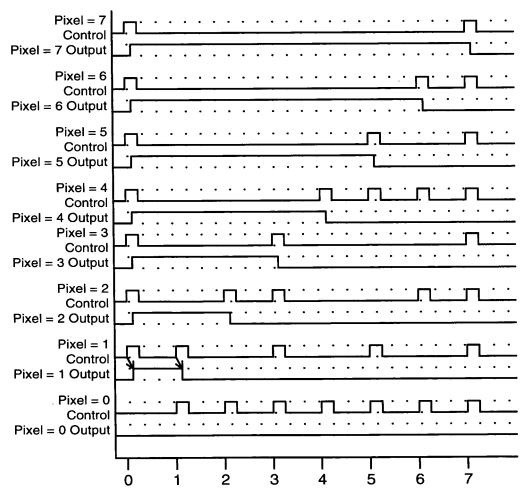
FIG. 3

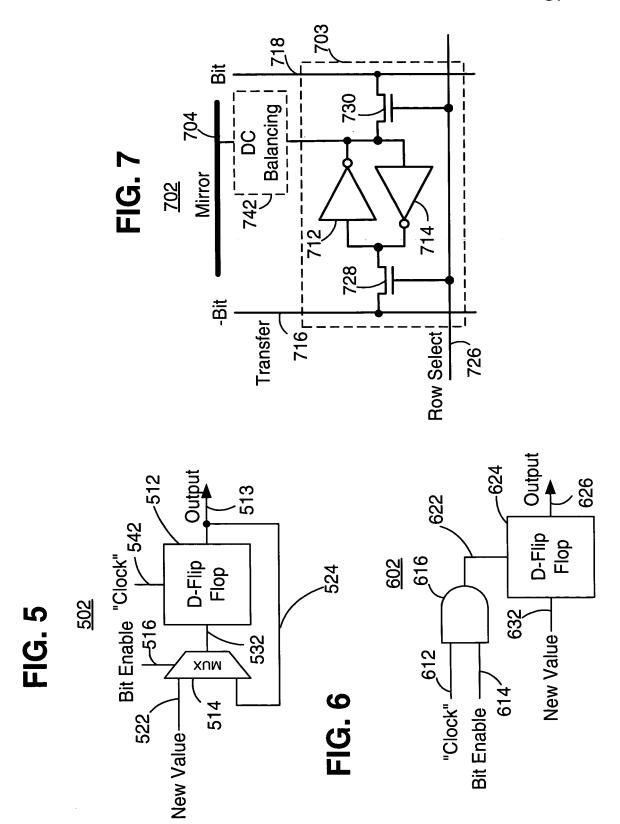
Co	ount 210	Read Bits
0	000	Read Bits 2,1,0
1	001	Read Bits 2,1
2	010	Read Bits 2,0
3	011	Read Bit 2
4		Read Bits 1,0
5		Read Bit 1
6		Read Bit 0
7	111	No reads

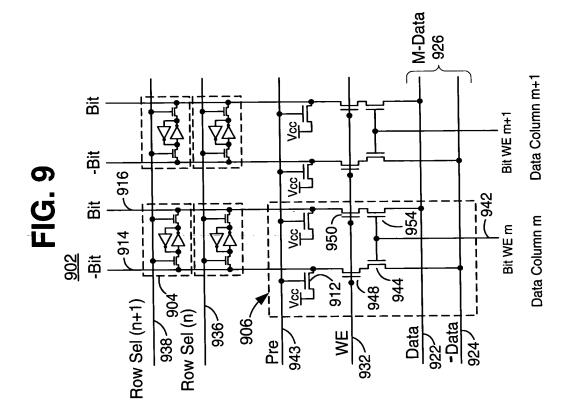
Decision to turn off Display-Bit

If all three are not 0, turn on output
If both 0, turn off output
If both 0, turn off output
If 0, turn off output, bit 2 memory is free
If both zero, turn off output
If 0, turn off output, bit 1 memory is free
If 0, turn off output, bit 0 memory is free
turn off output

FIG. 4







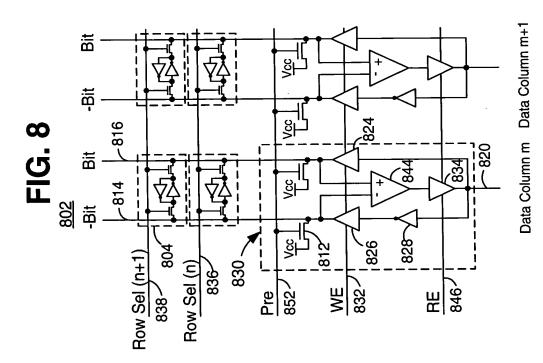
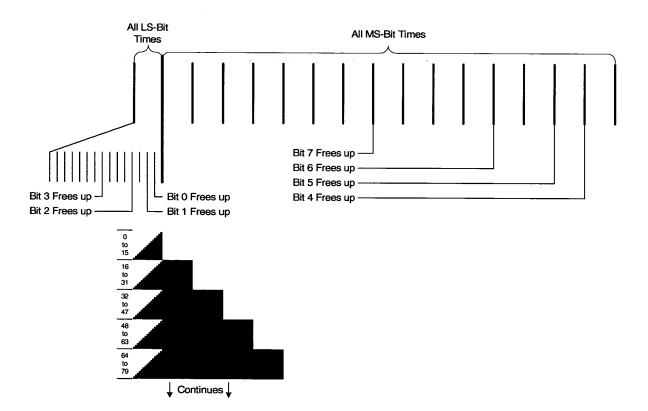


FIG. 10



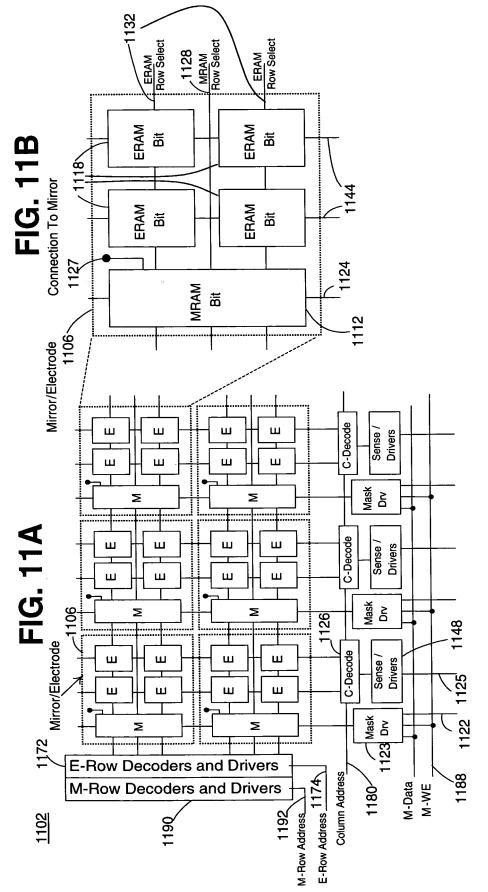


FIG. 12

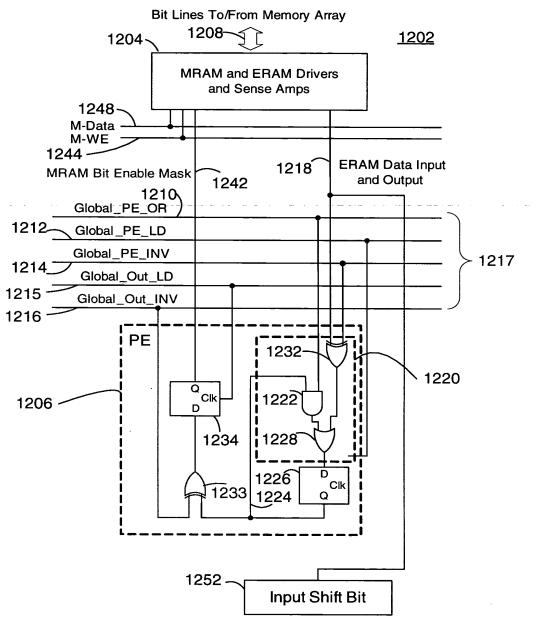


FIG. 13

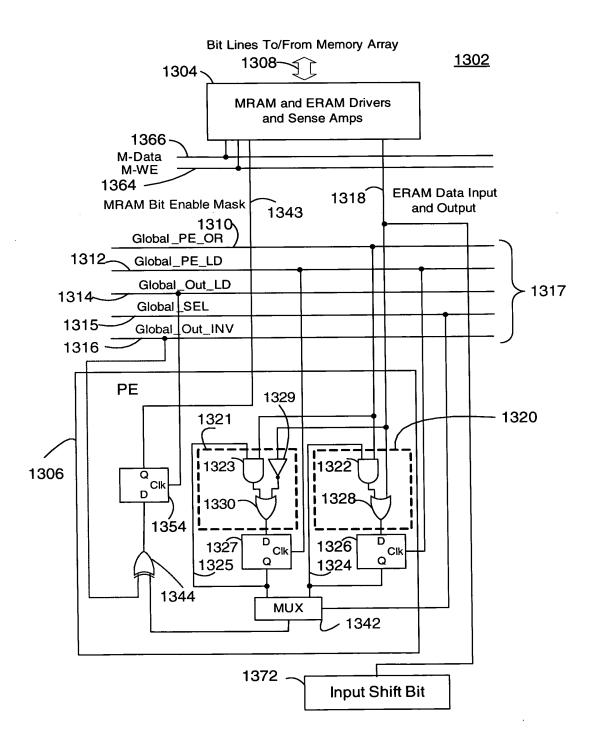


FIG. 14

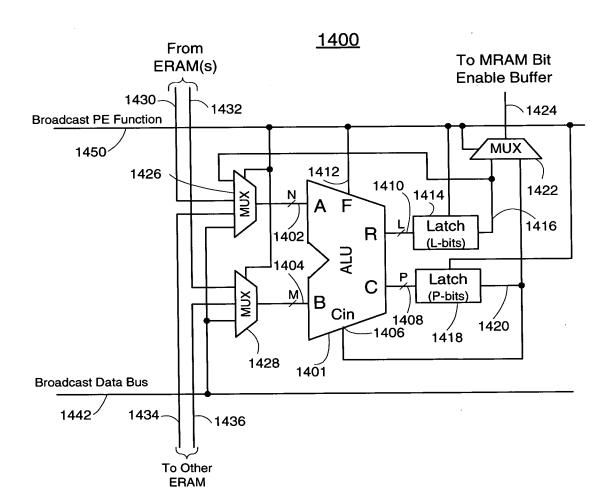
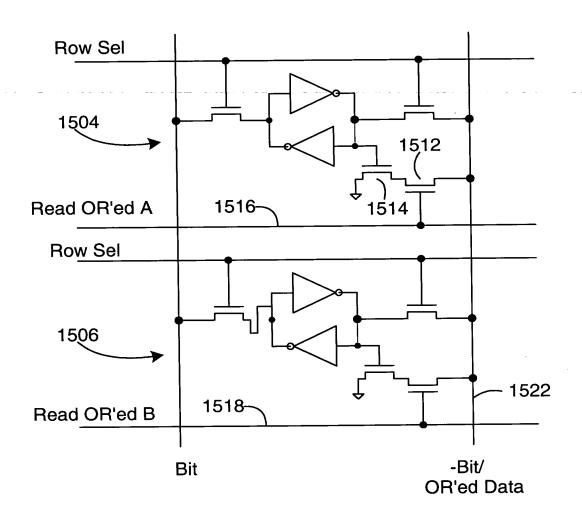
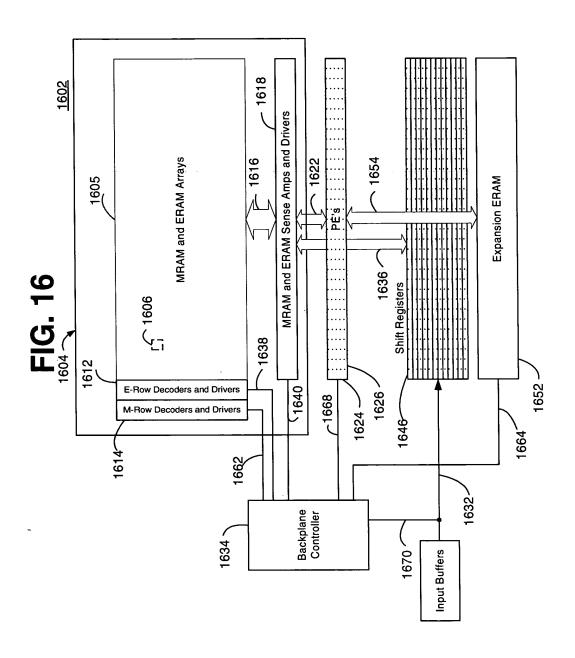


FIG. 15

<u>1502</u>





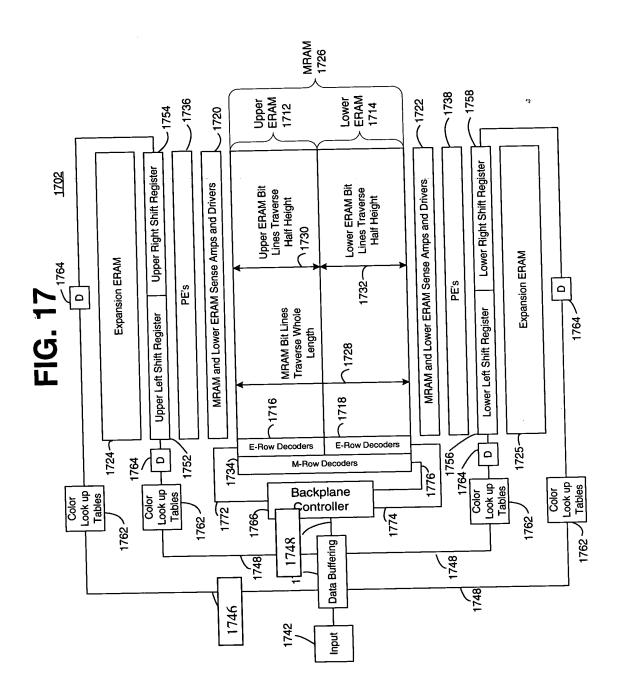


FIG. 18

Simple 2-LS Bit Count and 2-WS-Bit Split Count Example With LS-Bit Time = 4 Clocks and 17 Clocks Between Lines Un-Corrected

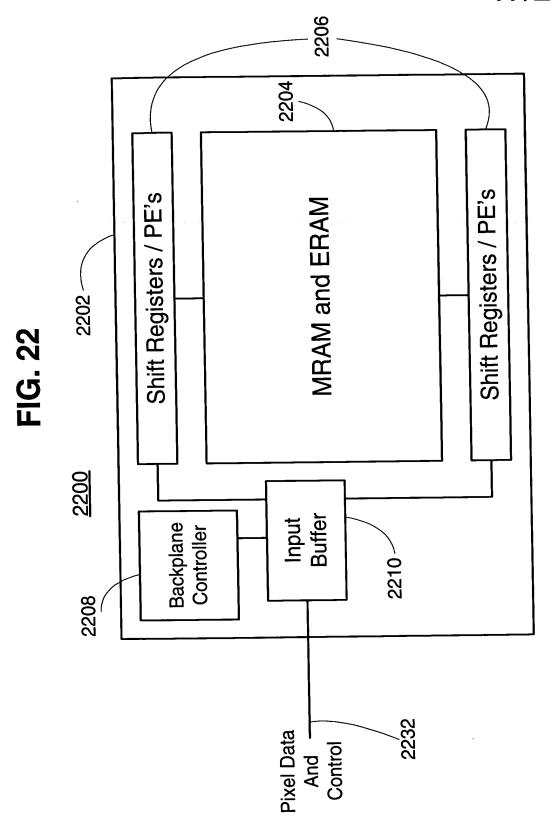
Simple			MS MRAM LS ERAM Reads						MS ERAM Reads												
	<u> </u>	SMRA	M							LSB	_			_		MHE	acos		I CO Torre	 	
Count#	3		1	0	0		_2			3	2	<u> </u>	4	<u>q</u>	_1	2	3		LSBTime=	17	
Binary Count	#####	#####	#####	####	####	#####							1	_	_				Line Time=	 '' 	
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Adjusted Time	0	4	8	12	13	28	44						╨	_			_				
Offset	0	0	0	C		d	0	q	WOOK,	0	0		9	q	q	9	0		Write	Read	Read
Set/Reset Disp.	S	S	S	R	S	R	R	R			_		Ш	4				Pattern	Conflict	Pattern	Conflict
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FIG. 19

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nary Count			######		####	####	####	####									1_		Line Time=	13	
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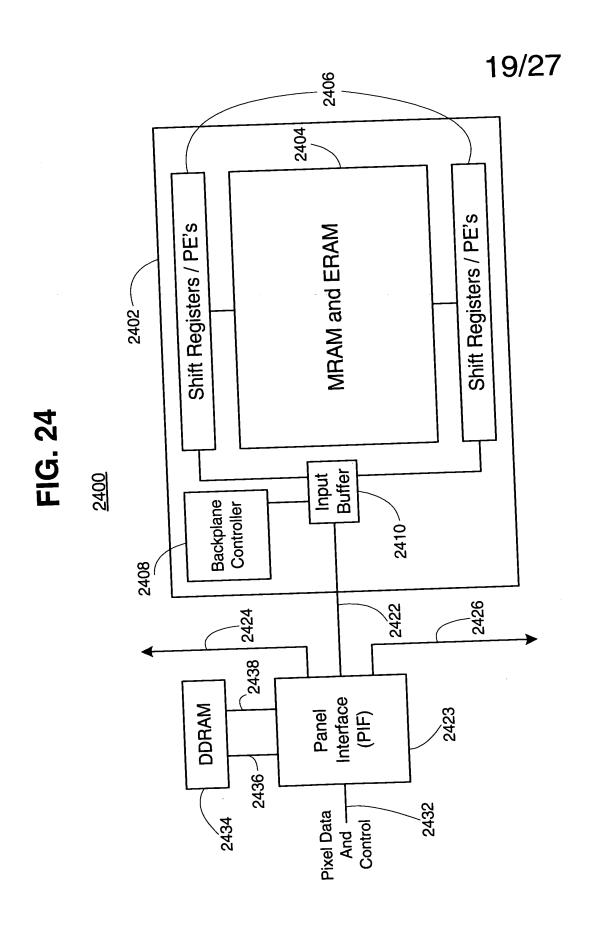
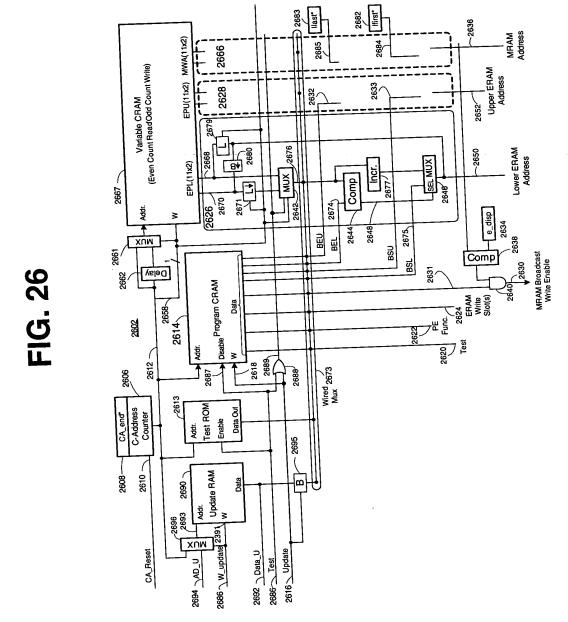


FIG. 25

	Pinon/			Number
Pixel	Binary	D4	P2	Reads
Value	Bits 9-6	P1	***************************************	
0	0000	0	0	2
1	0001	0	1	2
2	0010	0	1	2
3	0011	0	1	1
4	0100	1	0	2 2 2
5	0101	1	1	2
6	0110	1	1	A CONTRACTOR OF THE PROPERTY O
7	0111	1	1	1
8	1000	1	C	
		1	1	
10)	1	2
11		-	1	1
12	1100)	1 (1
13	uma francosamiento anticoloria de la companio de la	1	1	1
14		0	1	1 1
1.		1	1	1 0
				-
Total Zeros	3	2	Tota	1 24

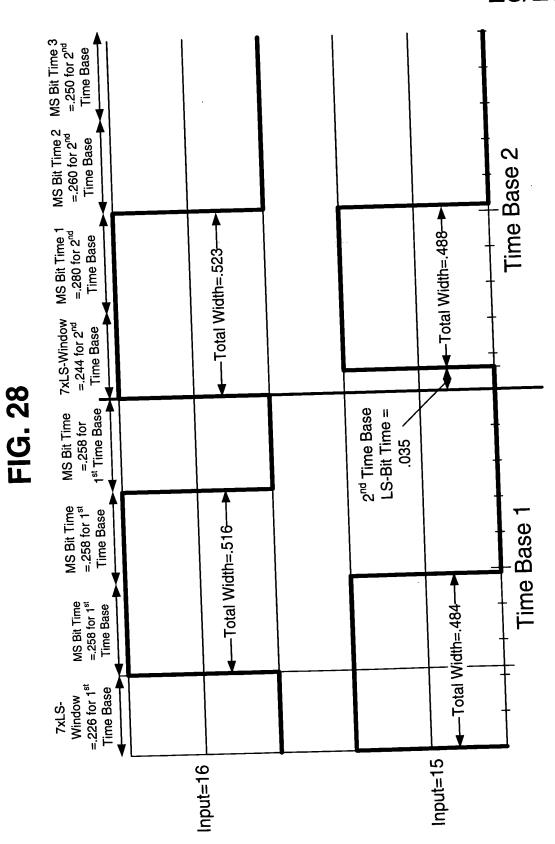


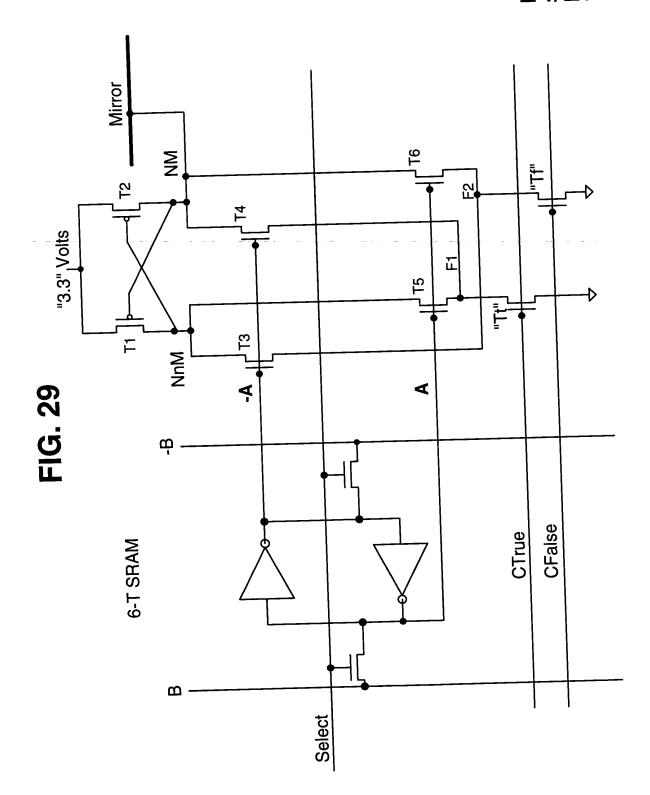
2682 Hirst*

FIG. 27

Simple Dual Time Base Example (3-LS and 2-MS Example)

			Simple D	ual Tin	ne Ba	se Exa	imple (3-	<u> </u>	10 Z-I	VIO EXC	unpie	· /		
Input Value	F	irst '	Time Base	e (Simpl	e Bina	ary)			Delta					
	T			Bit		Cumm.	i l	ł	ì	Bit	Time	Cumm.	First-	%
	MS	LS	Time	Position	Value	Value	Time	MS	LS	Position		Value	Second	Diff
- 0	0	- 0	0.000	LS=0	0	0.000	0.000	0	0	LS=0	0	0.000	0.000	
1	 	- 1	0.032	LS=1	0.032	0.032	0.035	<u> </u>	1	LS=1	0.035	0.035	-0.003	
2	Ö	2	0.065	LS=2	0.033	0.065	0.070	0	2	LS=2	0.035	0.070	-0.005 -0.008	
3	1 0	3	0.097	LS=3	0.032	0.097	0.105	0	3	LS=3	0.035		-0.008	
4	ō	4	0.129	LS=4	0.032	0.129	0.139	0	4	LS=4_	0.034	0.139	-0.010	
5	0	5	0.161	LS=5	0.032	0.161	0.174	0	5	LS=5_	0.035		-0.015	
6	0	6	0.194	LS=6	0.033	0.194	0.209	0	6	LS=6	0.035		0.017	
7	0	7	0.226	LS=7	0.032	0.226	0.209	0	6	LS=7	0.035		0.017	-
8	1	0	0.258	MS=0	0.000		0.244	0	7	MS=0	0.000		0.014	_
9		1	0.290	MS=1	0.258	0.258	0.279	1	0	MS=1	0.279		0.009	_
10	1	2	0.323	MS=2	0.258			1	1	MS=2	0.261			_
11	1	3	0.355	MS=3	0.258	0.774	0.349	1	2	MS=3	0.251	0.791	0.003	_
12	1	4	0.387		<u> </u>		0.384		3		+	┼	0.001	
13	1	5	0.419		<u> </u>	ļ	0.418		5		┼		-0.00	_
14	1	6		I	<u> </u>	<u> </u>	0.453	1	_		+'	 	-0.004	_
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16					<u> </u>	<u> </u>	0.523				+		0.00	
17		1	0.548		 		0.540				+	+	0.00	-
18						<u> </u>	0.575 0.610				+	+	0.00	_
19					 		0.645			H	+	+	0.00	
20		2 4				 	0.679				+		-0.00	2 -0.3
21		2 5			-		0.075			 	+	1	-0.00	4 -0.6
22		2 6			+-		0.749	-		31	_		-0.00	7 -1.0
23		2 7					0.784	_		///			-0.01	0 -1.3
24	_	3 0			+-	+	0.79			 			0.01	5 1.9
25		3 1					0.820	_		11	1		0.01	3 1.5
26		3 2	0.839			+	0.86			2			0.01	
27					+-	+	0.89			3			0.00	
28			0.900		+	+	0.93	_		411			0.00	
29			0.96		+-	+	0.96			5			0.00	
30			7 1.000				1.00			6			0.00	0.0
3	ш_	<u> </u>	7 1.000	<u> - </u>			11							





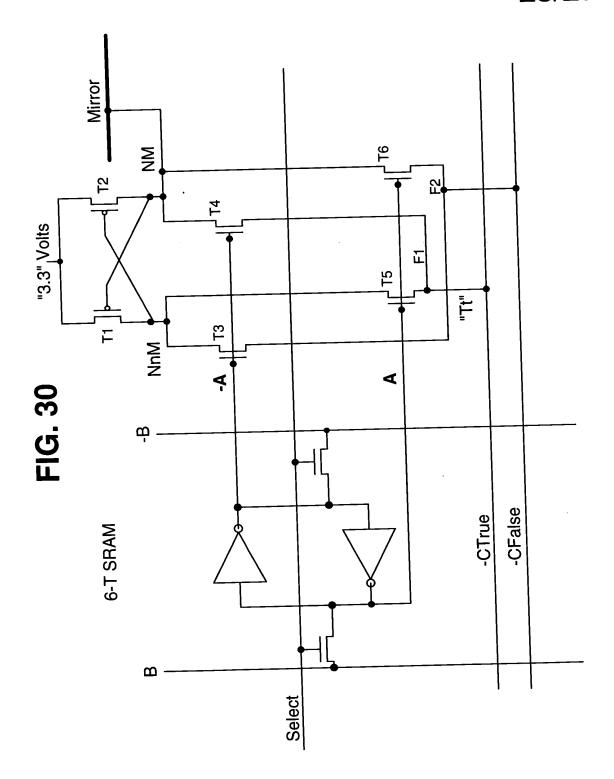


FIG. 31

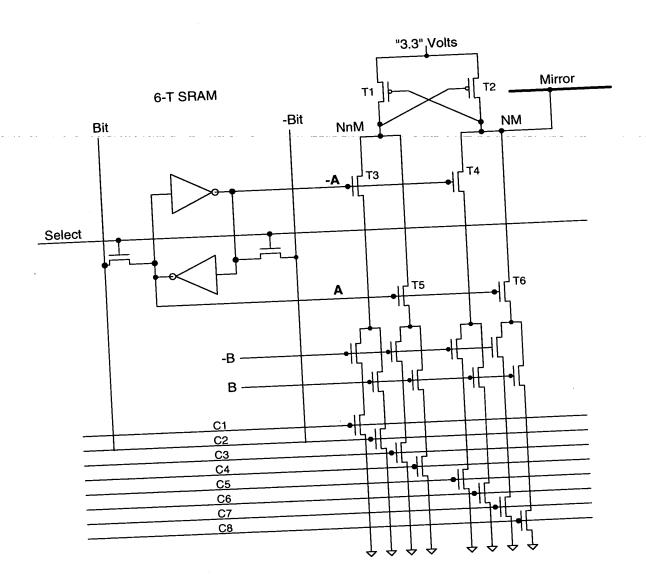


FIG. 32

